

Date: November 28, 2001

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FORM PTO-1449 (Colb)	ATTY DOCKET NO. 03022/37258	SERIAL NUMBER 09/918,347
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS' INFORMATION STATEMENT	APPLICANT Yishay MANSOUR, et al.	
	FILING DATE July 30, 2001	GROUP ART UNIT (N/A)

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Examiner's Initials		DOCUMENT NO.	DATE	NAME	CLASS	SUB	FILING DATE
UP	AA	5,541,912	July 1996	Choudhury, et al.			

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↑	AC	Noboru Endo, et al., "Shared Buffer Memory Switch for an ATM Exchange", IEEE Transactions on Communications, Vol. 41, No. 1, January 1993, pp. 237-245.
	AD	Marek I. Irland, "Buffer Management in a Packet Switch", IEEE Transactions on Communications, Vol. COM-26, No. 3, March 1978, pp. 326-377.
	AE	Farouk Kamoun, et al., "Analysis of Shared Finite Storage in a Computer Network Node Environment Under General Traffic Conditions", IEEE Transactions on Communications, Vol. COM-28, No. 7, July 1980, pp. 992-1003.
GF	AF	Guy Latouche, "Exponential Sharing a Finite Storage: Comparison of space Allocation Policies", IEEE Transactions on Communications, Vol. COM-28, No. 6, June 1980, pp. 910-915.
CP	AG	G. J. Foschini, et al., "Sharing Memory Optimally", IEEE Transactions on Communications, Vol. COM-28, No. 3, March 1983, pp. 352-360.
C	AH	A. K. Choudhury, et al., "Dynamic queue length Thresholds for Shared-Memory Packet Switches", IEEE/ACM Transactions on Communications, Vol. 6, No. 2, April 1998, pp. 130-140.
J	AI	Harry G. Perros, et al., "Call Admission Control Schemes: A Review", IEEE Commun. Mag., November 1996, pp. 82-91.
	AJ	Ashok Erramili, et al., "Monitoring Packet Traffic Levels", Proc. IEEE GLOBECOM '94, Vol. 1, December 1994, pp. 274-280.
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DATE CONSIDERED: 10/5/05

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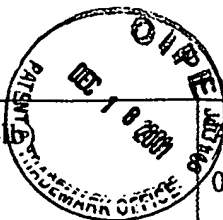
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G	AG	Guo-Liang Wu, et al., "A Buffer Allocation Scheme for ATM Networks: Complete Sharing Based on Virtual Partition", IEEE/ACM Trans. Networking 3:6, December 1995, pp.660-670.
CP	AH	Yonghwan Kim, et al., "Timescale of Interest in Traffic Measurement for Link Bandwidth Allocation Design", IEEE INFOCOM '96, March 1996, pp. 738-748.
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J	AJ	H.M. AboElFoton, et al., "An Integrated Call Admission and Buffer Management Scheme for LAN Interconnection Over a Shared-Memory ATM Switch", Comp. Commun. 21, 1998, pp. 697-714.
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↑	AE	Ashok Erramilli, et al., "Self-Similarity in High-Speed Network traffic Measurements: Fact or Artifact?", 12 th Nordic Teletraffic Seminar NTS 12(VTT Symposium 154), August 1995, pp. 299-310.
↑	AF	Simon Fong, et al., "Performance Evaluation of Shared-Buffer ATM Switches Under Self-Similar Traffic", 1997 IEEE Int. Performance, Computing and Comm. Conf. February 1997, pp. 252-258.
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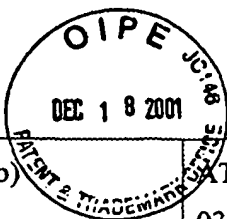
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↑	AF	M. G. Hluchyj, et al., "Queueing in high-performance packet switching", IEEE J. Select. Areas Commun., Vol. 6, pp. 1587-1597, December 1988.
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	AH	Walter Willinger, et al., "Self-similarity through high-variability: statistical analysis of ethernet LAN traffic at the source level", ACM SIGCOMM'97.
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